



Re the Application of:

MARUO

Serial No. 09/687,197

Filed: October 14, 2000

For: SEMICONDUCTOR DEVICES

AND METHODS FOR

MANUFACTURING THE SAME

Group Art Unit: 2822

Examiner: Duong, K.

15.2015332  
#7/Election  
9/4/02  
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TECHNOLOGY CENTER 2800

**RESPONSE TO ELECTION/RESTRICTION REQUIREMENT**

Assistant Commissioner for Patents  
Washington, DC 20231

Dear Sirs:

In response to the Election/Restriction Requirement dated July 17, 2002, the response being due by Monday, August 19, 2002 (August 17 was a Saturday), please enter and consider the following.

Applicant hereby elects to prosecute Group I, claims 1-10 and 26, drawn to methods for manufacturing a semiconductor device.

Examination on the merits is respectfully requested. It is believed that no fees are due in connection with this paper. If however, any such fees are due, the Commissioner is hereby authorized to charge payment of the fees associated with this paper to Deposit Account No. 50-0585. A copy of this paper is enclosed.

Respectfully submitted,

*Alan S. Raynes*

Alan S. Raynes

Reg. No. 39,809

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Dated: August 19, 2002

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**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on August 19, 2002.

*Alan S. Raynes*

Alan S. Raynes

August 19, 2002  
(Date)